

09,854,177.

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,759,255 B2  
DATED : July 6, 2004  
INVENTOR(S) : Xu et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 23,

Line 31, after the phrase "conductor substrate," please delete "comprise" and substitute therefor -- comprises --.

Line 63, after the phrase "comprises approximately," please delete " $-1 \times 10^{31} \text{ }^{16} \text{ C/cm}^2$ " and substitute therefor --  $-1 \times 10^{-6} \text{ C/cm}^2$  --.

Column 24,

Line 9, after the phrase "and wherein," please delete "deposing" and substitute therefor -- depositing --.

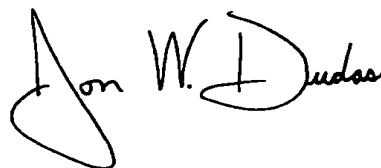
Line 41, please delete "annealing field" and substitute -- a tunneling field --.

Column 25,

Line 33, after the phrase "the semiconductor," please delete "substate" and substitute therefor -- substrate --.

Signed and Sealed this

Seventh Day of December, 2004

A handwritten signature in black ink, reading "Jon W. Dudas". The signature is stylized, with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS

*Director of the United States Patent and Trademark Office*